

FORM 1449* INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary)	Docket Number: 10873.589USWO	Application Number: 09/700,940
	Applicant: SAKIYAMA et al.	
	Filing Date: November 21, 2000	Group Art Unit: 2812 2825

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CME	5,182,223	Jan. 1993	Ovens et al.	—	—	—
CME	5,739,570	April 1998	Ovens et al.	—	—	—
CME	5,631,492	May 1997	Ramus et al.	—	—	—

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
CME	5-21711	January 1993	Japan	—	—	Refer to IDS	
CME	5-283615	October 1993	Japan	—	—	partial & abstract	
CME	7-106521	April 1995	Japan	—	—	abstract	
CME	10-150148	June 1998	Japan	—	—	abstract	
CME	10-107235	April 1998	Japan	—	—	abstract	
CME	10-326833	Dec. 1998	Japan	—	—	partial & abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CME	—	"di/dt Noise in CMOS Integrated Circuits"; Larsson, P.; Analog Intergrated Circuits and Signal Processing; Vol. 14, pp. 113-129; 1997
		Verification of Translation



EXAMINER <i>C. Luerhart</i>	DATE CONSIDERED <i>1-16-02</i>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	